2007 IEEE International Symposium on Circuits and Systems (ISCAS 2007)

FAX TO: +1 312 604 7376			REGISTRATION FORM				-Registration Deadline February 17, 2007		
Title: Prof. Dr.	Mr.	Ms.	Mrs	S.					
First Name:		Middle	Initial [.]		Last/Family Nar	no.			
		Ivildule	miliai.			ne.			
Company/Institution:									
Address:			Count						
City/State/Province:	City/State/Province:					Zip/Postal	Code:		
E-mail:					EEE Members	nip #:			
Tel: + ()				Fax: + ()			
Country Code Area Code	Number				Country Code	Area Code Number			
Please let us know if you have an	y disability	and/or any sp	ecial diet	tary require	ements:				
REGISTRATION FEES At least	one author o		st register a	at a non-stud		ary 17, 2007			
Category	Early, by Feb. 17, 2007		Late, or on-site		Amount	All the	bes of registration include:		
ember, IEEE	590		690				es or registration molude:		
on-member, IEEE	715		815			•	All sessions		
tudent member, IEEE	300		400			•	Proceedings CD-ROM		
tudent non-member, IEEE fe member, IEEE	300		450 400 690			•	 Welcome reception Coffee breaks 		
tudent registering paper(s)						•	Banquet		
	rials				•	Farewell party			
ach Half-Day Tutorial, Non-student	250			300					
ach Half-Day Tutorial, student		200		250					
OPTIONAL ADDITIONAL ITEMS	Quantity		Amount		Subtotal		·		
	rials CD-ROM		50 50			Please indicate your paper numb For more than 4 papers, the registration fee **			
ditional Proceedings CD-ROM ditional Welcome Reception Ticket			40						
litional Banquet Ticket			85						
dditional Farew ell Party Ticket			4(0		**		
	Total Am	ount for Addit	ional Ite	ms: \$					
TUTORIALS Please indicate bel		-	attend. Pl	ease see a		-	orial fees. ay 27, 1pm - 4pm		
T01: Integrated Biosensors				□ T0	T08: Sensor Netw orks				
T02: Satellite Navigation Receiver Design : GPS and Beyond				T0	T09: QoS Path(s) Selection for Routing and Protection in Communication Netw orks				
T03: Programmable Wireless Networks (aka Cognitive Radio)				□ T1	T10: Multimedia Signal Processing on Personal Computers				
T04: Techniques for Operational Amplifier Speed and Accuracy									
 Improvement : Analog Circuit Design with Structural Methodology 					T11: Nano-Scale Implantable Micro-Stimulator SoC Design				
T05: H.264/MPEG-4 Part 10 Emerging Multimedia Coding Standard					T12: Challenges and Opportunities of Digital Design in Nanoscale CMOS				
T06: Design Challenges and Solutions for Nanoscale Memori			nories	an	T13: Analog Circuit Design on Digital CMOS : Why it is difficult, and w hich ideas help				
D T07: Sensor Networks: Applications, Technologies and Protocols				□ T1-	T14: Design of Digital Filters Satisfying Prescribed Specifications				
CANCELLATION AND REFUND PO Please check I have read and a					125 admin. fee. N ove and on the l				

By Credit Card.	Please circle one: Maste	rCard / VISA / AMEX			
I authorize you to charge my credit card with the amount of US Dollars:					
Card Number:					Expiry Date:
Cardholder's Na	me (In case the registra	nt is not the card holder	, please also fax a copy	of both sides of your of	credit card):

ID validation number (The last 3 digits of the number that can be found on the back side of your credit card):

Cardholder's Signature:

ISCAS 2007 Registration
c/o Alliance Management Group LLC 15 W. Marne Ave.
PO Box 265 Beverly Shores, IN 46301, USA

Date:

Tel: +1 219 871 0210 Fax: +1 312 604 7376 Email: <u>iscas-reg@amg-corp.com</u>